



# PJS05 SERIES

## 400W LOW CLAMPING VOLTAGE SINGLE TVS FOR PROTECTION

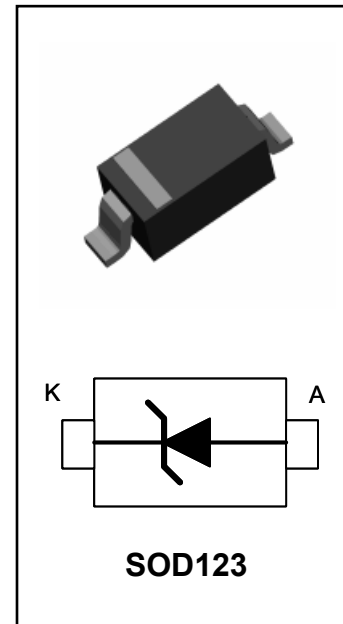
This TVS/Zener Series has been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in very sensitive CMOS circuitry operating at 5V, 12V, 15V and 24Vdc .These devices come in an industry standard SOD123 package making them suitable for Portable/Computing Electronics, where the board space is a premium.

### SPECIFICATION FEATURES

- 400W Power Dissipation (8/20 $\mu$ s Waveform)
- Very Low Leakage Current
- IEC61000-4-2 ESD 15kV air, 8kV Contact Compliance
- SOD123 Package
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. . (Halogen Free)

### APPLICATIONS

- Personal Digital Assistant (PDA)
- Digital Cameras
- Portable Instrumentation
- Mobile Phones and Accessories
- Desktops, Laptops



### MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Pulse Power (8/20 $\mu$ s Waveform)	$P_{pp}$	400	W
ESD Voltage (HBM)	$V_{ESD}$	25	kV
Operating Temperature Range	$T_J$	-55 to +125	$^{\circ}$ C
Storage Temperature Range	$T_{stg}$	-55 to +150	$^{\circ}$ C

### ELECTRICAL CHARACTERISTICS $T_j = 25^{\circ}$ C

#### PJS05 Marking T1S

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	$I_{BR} = 1 \text{ mA}$	6.0			V
Reverse Leakage Current	$I_R$	$V_R = 5 \text{ V}$			20	$\mu$ A
Clamping Voltage (8/20 $\mu$ s)	$V_c$	$I_{pp} = 5 \text{ A}$			7.5	V
Clamping Voltage (820 $\mu$ s)	$V_c$	$I_{pp} = 24 \text{ A}$			16	V
Off State Junction Capacitance	$C_j$	0 Vdc Bias f = 1MHz			550	pF
Off State Junction Capacitance	$C_j$	5 Vdc Bias f = 1MHz			235	pF



## PJSD05 Series

**ELECTRICAL CHARACTERISTICS**  $T_j = 25^{\circ}\text{C}$ **PJSD12 Marking T4S**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				12	V
Reverse Breakdown Voltage	$V_{BR}$	$I_{BR} = 1\text{mA}$	13.3			V
Reverse Leakage Current	$I_R$	$V_R = 12\text{V}$			1	$\mu\text{A}$
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 5\text{A}$			14.5	V
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 17\text{A}$			23	V
Off State Junction Capacitance	$C_j$	0 Vdc Bias $f = 1\text{MHz}$			180	pF

**PJSD15 Marking T5S**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				15	V
Reverse Breakdown Voltage	$V_{BR}$	$I_{BR} = 1\text{mA}$	16.7			V
Reverse Leakage Current	$I_R$	$V_R = 15\text{V}$			1	$\mu\text{A}$
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 5\text{A}$			19	V
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 14\text{A}$			28	V
Off State Junction Capacitance	$C_j$	0 Vdc Bias $f = 1\text{MHz}$			165	pF

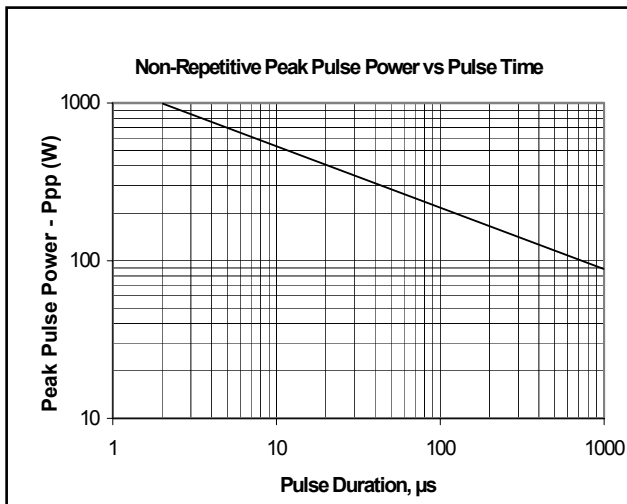
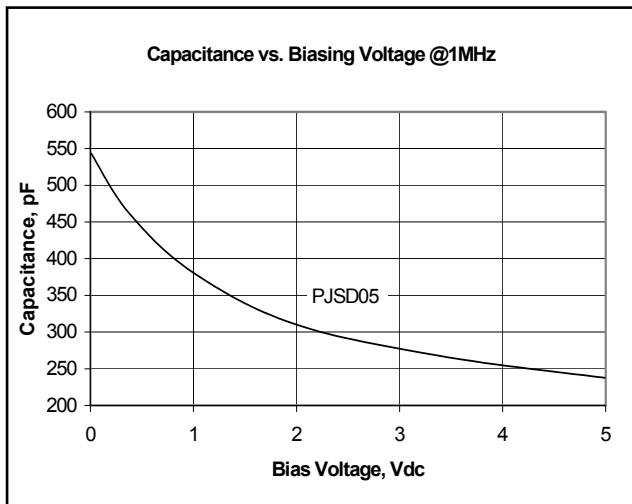
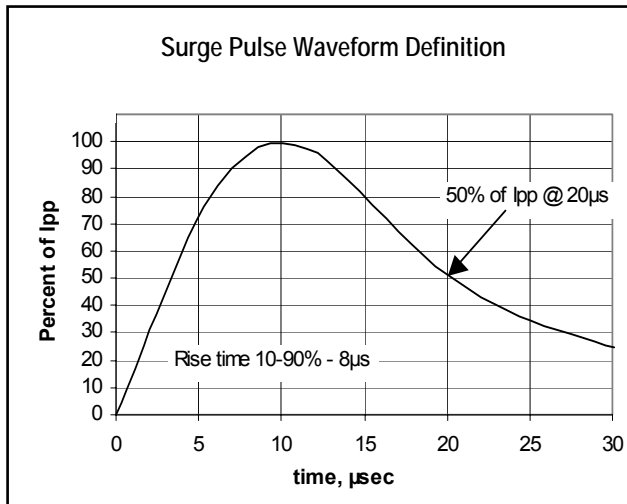
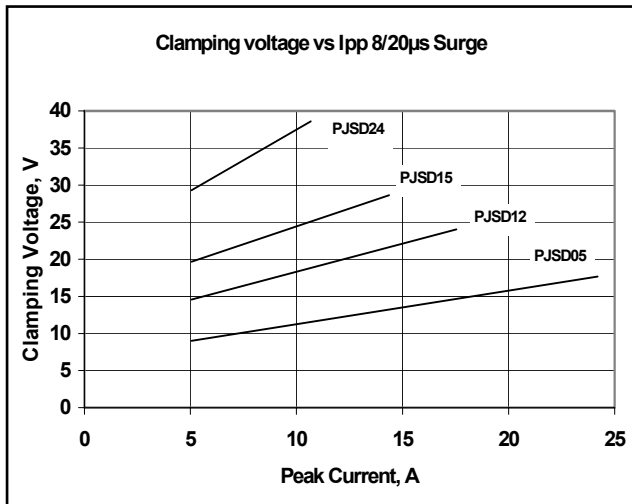
**PJSD24 Marking T6S**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{WRM}$				24	V
Reverse Breakdown Voltage	$V_{BR}$	$I_{BR} = 1\text{mA}$	26.7			V
Reverse Leakage Current	$I_R$	$V_R = 24\text{V}$			1	$\mu\text{A}$
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 5\text{A}$			29	V
Clamping Voltage (8/20 $\mu\text{s}$ )	$V_c$	$I_{pp} = 11\text{A}$			37	V
Off State Junction Capacitance	$C_j$	0 Vdc Bias $f = 1\text{MHz}$			120	pF



# PJSD05 SERIES

## TYPICAL CHARACTERISTICS



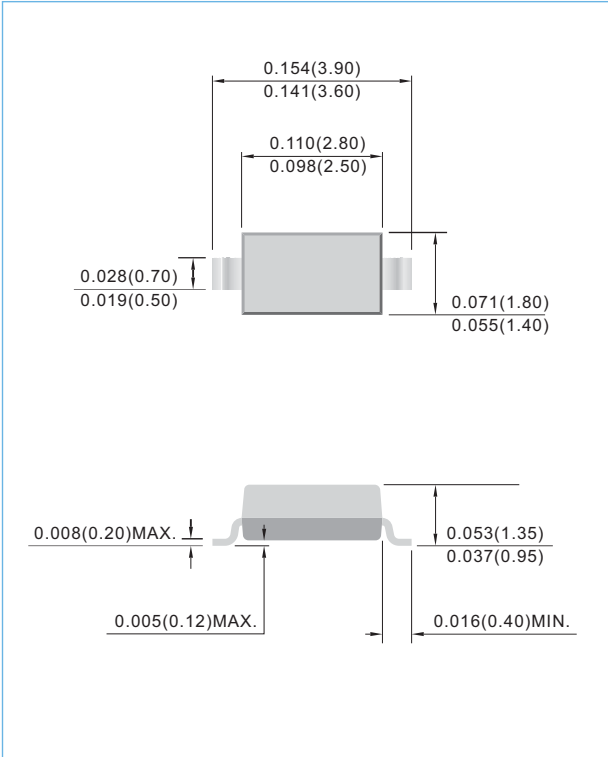


# PJSD05 SERIES

## PACKAGE DIMENSIONS AND BOND PAD LAYOUT

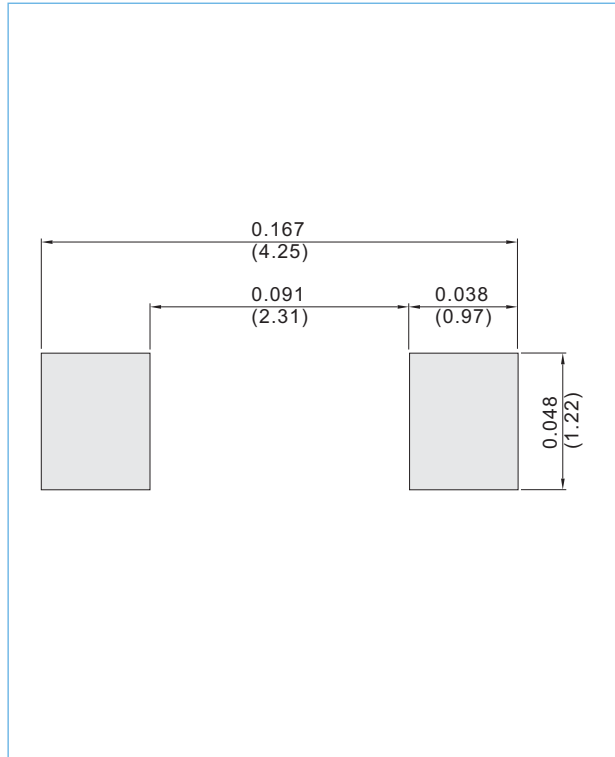
### SOD-123

Unit : inch(mm)



### SOD-123

Unit : inch(mm)





# PJSD05 SERIES

## Part No\_packing code\_Version

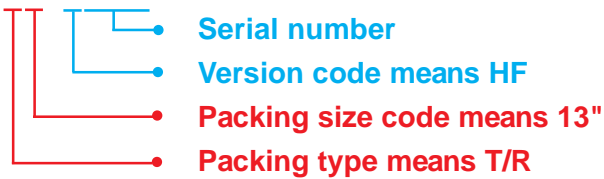
PJSD05\_R1\_00001

PJSD05\_R2\_00001

For example :

**RB500V-40\_R2\_00001**

Part No.



Packing Code XX				Version Code XXXXX		
Packing type	1 <sup>st</sup> Code	Packing size code	2 <sup>nd</sup> Code	HF or RoHS	1 <sup>st</sup> Code	2 <sup>nd</sup> ~5 <sup>th</sup> Code
Tape and Ammunition Box (T/B)	A	N/A	0	HF	0	serial number
Tape and Reel (T/R)	R	7"	1	RoHS	1	serial number
Bulk Packing (B/P)	B	13"	2			
Tube Packing (T/P)	T	26mm	X			
Tape and Reel (Right Oriented) (TRR)	S	52mm	Y			
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP (PBCU)	U			
FORMING	F	PANASERT T/B CATHODE DOWN (PBCD)	D			



## PJSD05 SERIES

### Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.